



QIS INSTITUTE OF TECHNOLOGY :: ONGOLE

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)
(An ISO 9001:2015 Certified Institution)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Sample CO & PO Mapping

Year: IV Year Sem:I

Regulation:R16

Course Name:Digital Signal Processing

"At the end of this course, the student will be able to"

S. No.	Name of Course	Course Outcomes	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	
1	Digital Signal Processing	C.324.1	Apply the difference equations concept in the analyziation of Discrete time systems	3	3	1									2	2	
		C.324.2	Use the FFT algorithm for solving the DFT of a given signal	3	2	1										2	2
		C.324.3	Design a Digital filter (FIR&IIR) from the given specifications	3	3	2		2				2			2	2	2
		C.324.4	Realize the FIR and IIR structures from the designed digital filter.	3	2	2		2				2			2	2	2
		C.324.5	Use the Multirate Processing concepts in various applications(eg: Design of phase shifters, Interfacing of digital systems...)	3	2	2		2					1		1	2	2
		C.324.6	Apply the signal processing concepts on DSP Processor.	3	1	1		3					2			2	2